



XIN



Fig.1 High Output Rail Voltage vs Ambient Temperature

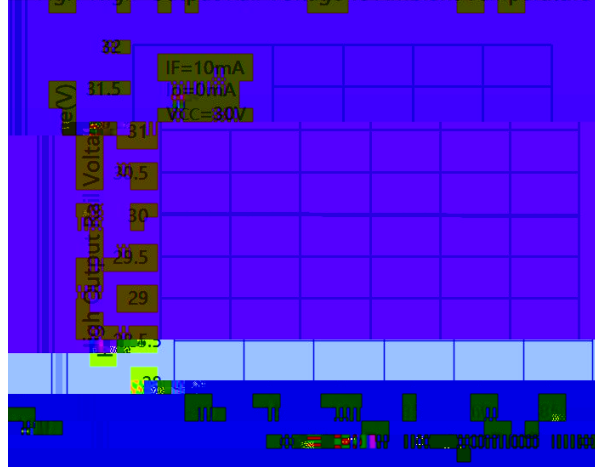


Fig.2 Low Level Output Voltage vs Ambient Temperature

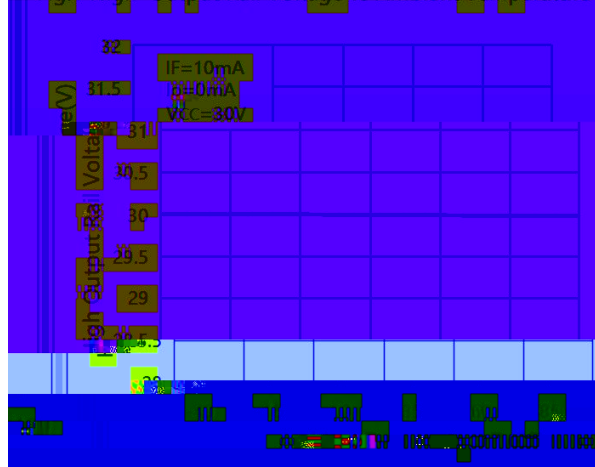


Fig.3 Low Level Output Voltage vs Ambient Temperature



Fig.4 Supply Current vs Ambient Temperature

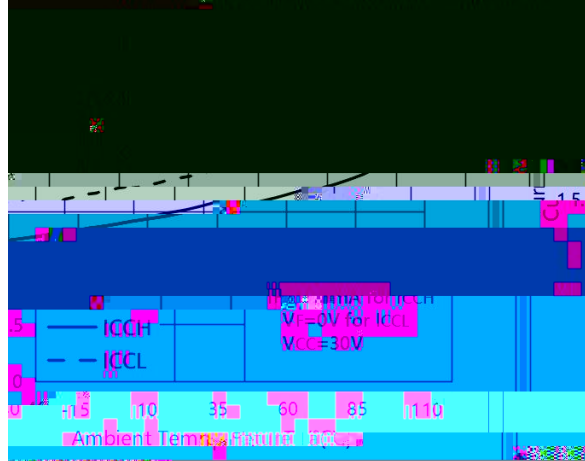


Fig.5 Supply Current vs Supply Voltage

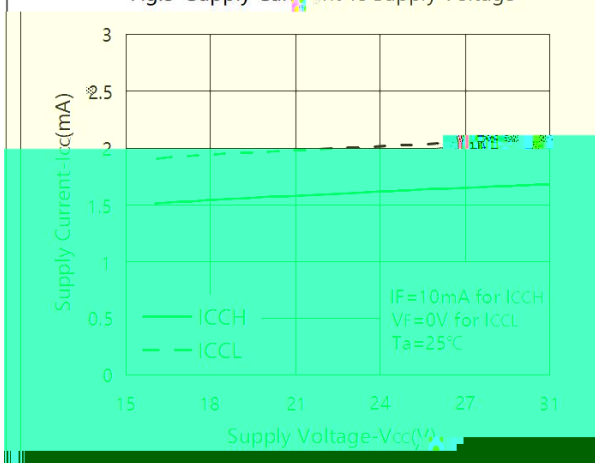
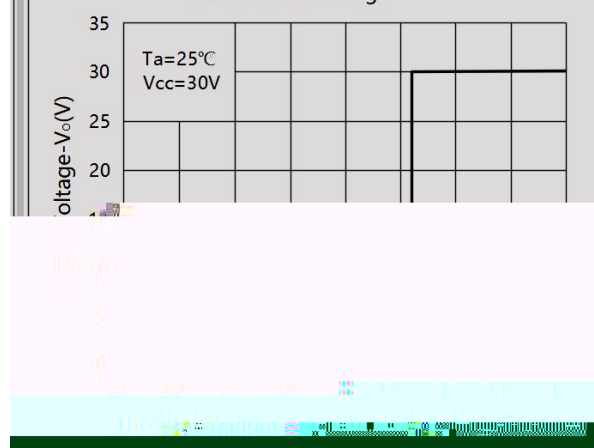


Fig.6 Output Voltage vs Threshold Input Current Low to High







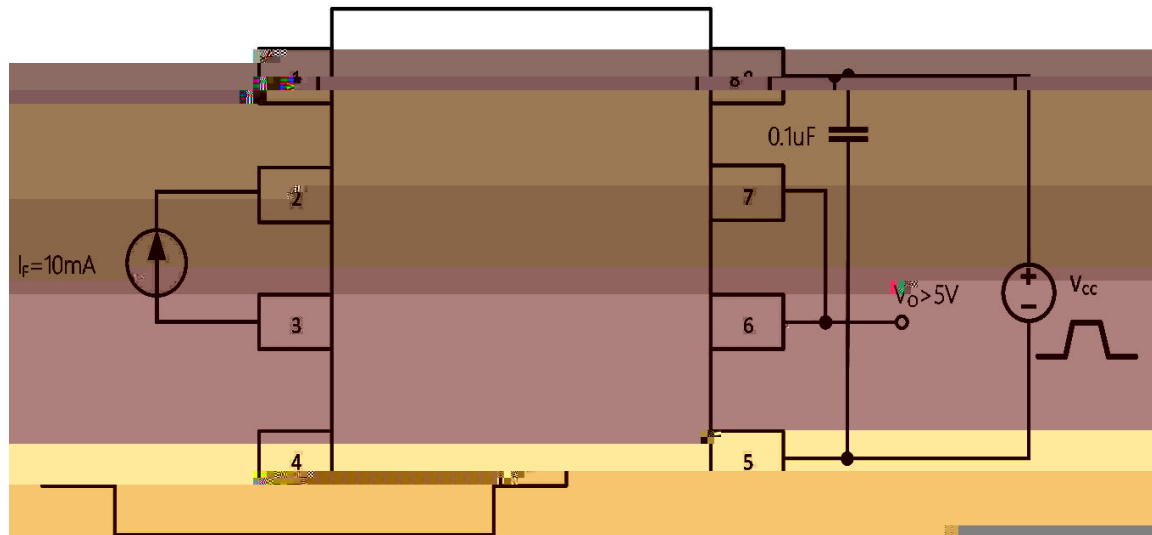


Fig.17 UVLO Test Circuit

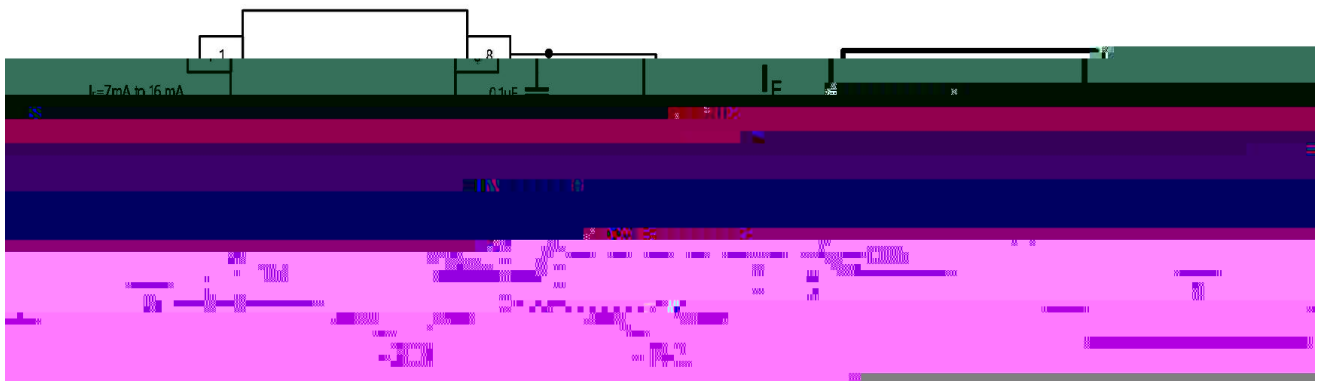


Fig.18 T_{PHL} T_{PLH} T_R T_F Test Circuit

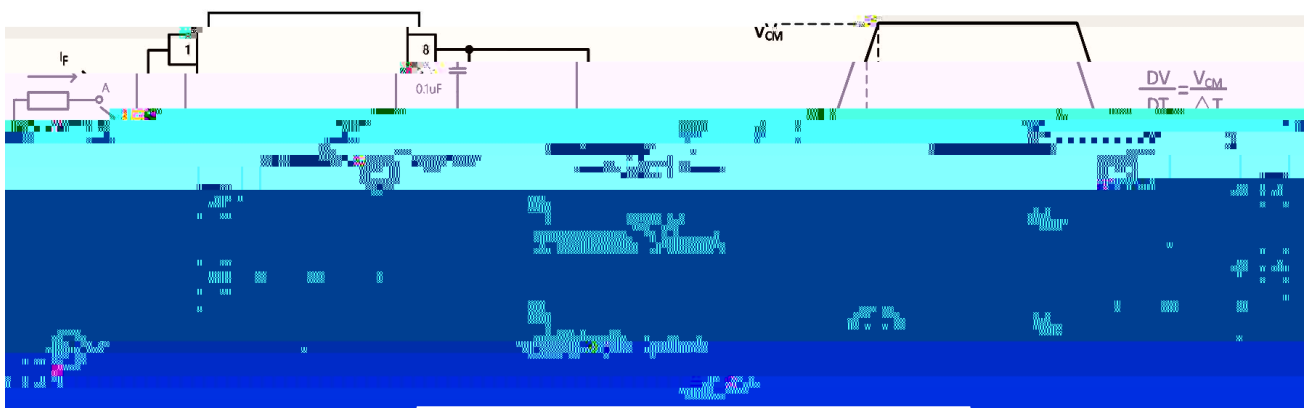


Fig.19 CMR Test Circuit



Symbol	Min	Max	Unit
--------	-----	-----	------

