



- 50 kV/ μ s minimum Common Mode Rejection
- 15V ~ 30V Wide operating VCC Range
- Maximum peak output current 2.5A
- The output stage can drive IGBTs with 150A and 1200V
- Creepage distance > 7.0mm
- Operating Temperature: -55 ~ 110
- Environmentally friendly products, compliant with CQC, UL, and VDE requirements

- Case: WSOP16
- Soldering Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solder ability-per MIL-STD-202, Method 208

- Industrial Automation Equipment Used for inverters and AC servo drives to improve system reliability and energy efficiency





XINGLIGHT®





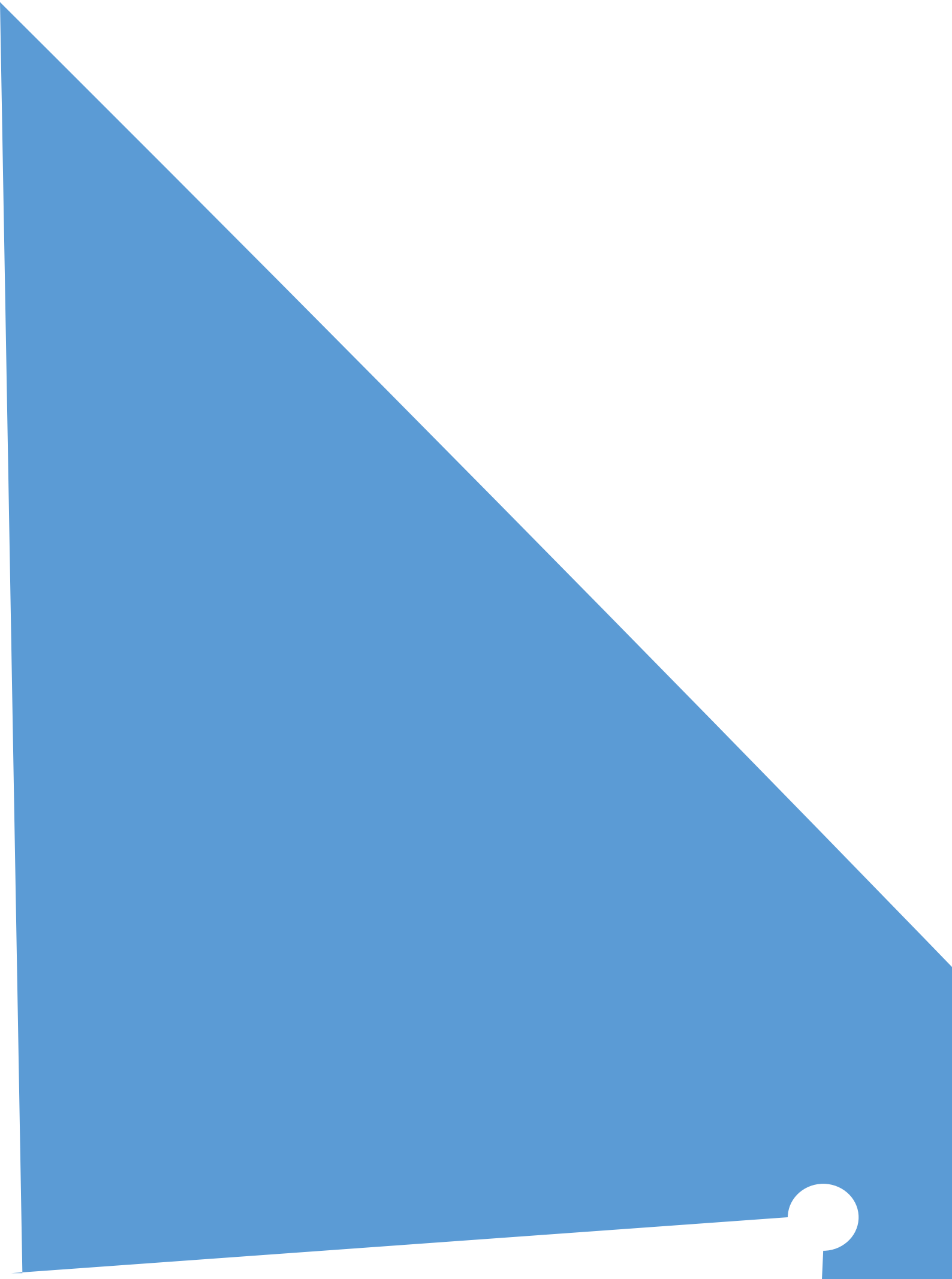
Unless otherwise noted, all typical values at TA = 25°C, VCC2 = 30 V, VEE = Ground; All Minimum/Maximum specifications are at Recommended Operating Conditions.

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Propagation Delay Time to High Output Level	tPLH	Rg = 10 Ω, Cg = 10nF, f = 10 kHz, Duty				









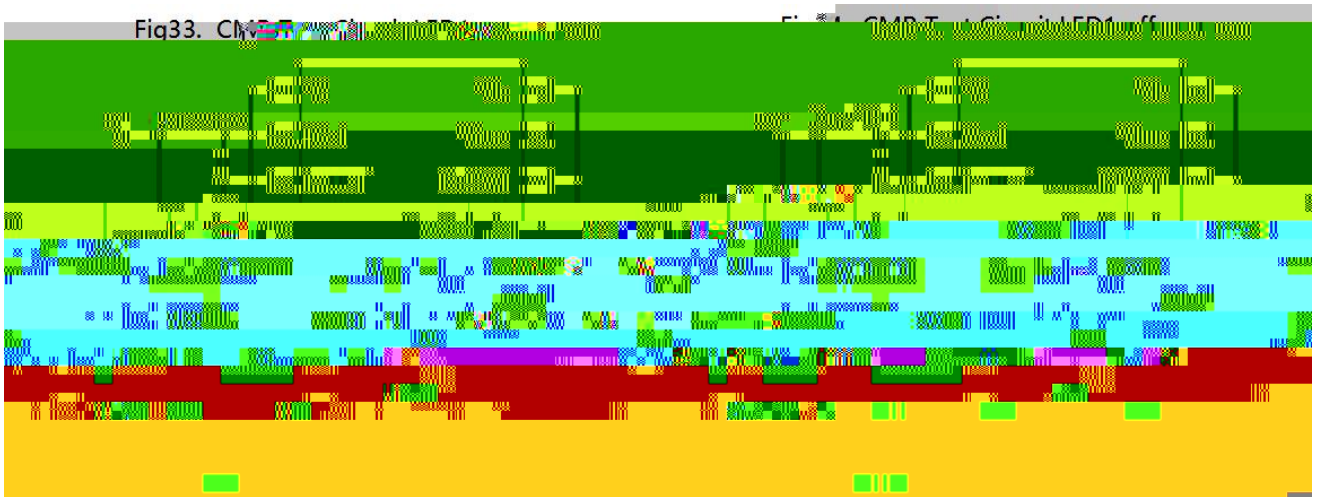
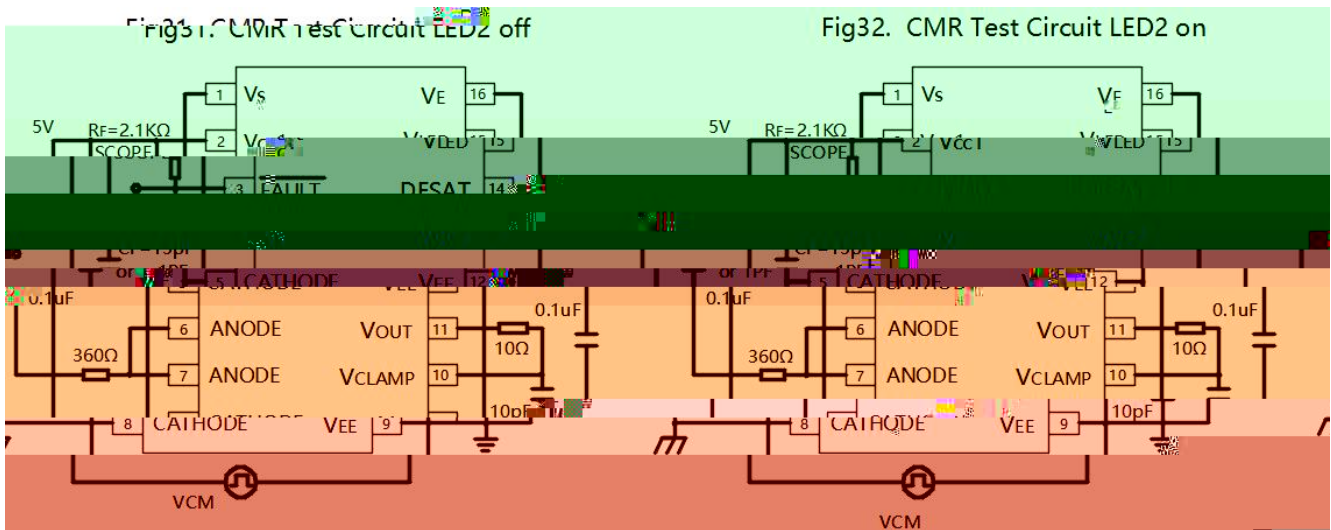


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1 Product Overview Description

The AT332J are highly integrated power control devices that incorporates all the necessary components for a complete, isolated IGBT / MOSFET gate drive circuit with fault protection and feedback into one SOP16 package. Active Miller clamp function eliminates the need of negative gate drive in most application application and allows the use of simple bootstrap supply for high side driver. An optically isolated power output stage drives IGBTs with power ratings of up to 150A and 1200V. A high speed internal optical link minimizes the propagation delays between he micro controller and the IGBT while allowing the two systems to operate at very large common mode voltage differences that are common in industrial motor drives and other power switching applications. An output IC provides local protection for the IGBT to prevent damage during over current, and a second opticalink provides a fully isolated fault status feedback signal for the micro controller. A built in " watchdog " circuit, UVLO monitors the power stage supply voltage to prevent IGBT caused by insufficient gate drive voltages. This integrated IGBT gate driver is designed to increase the performance and reliabi lity of a motor drive without the cost, size, and complexity of a discrete design.





4 Output Control

The outputs (VOUT and FAULT) of the AT332J are controlled by the combination of IF, UVLO and a detected IGBT Desat condition. Once UVLO is not active ($V_{CC2} - V_E > V_{UVLO}$), VOUT is allowed to go high, and the DESAT (Pin14) detection feature of the AT332J will be the primary source of IGBT protection. Once VCC2 is increased from 0V jD















